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NPIC/TDS/D-1091-67
24 October 1967

MEMORANDUM FOR THE RECORD

SUBJECT: [] Chip Comparator Progress Report

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1. During the period 2-4 August [] personnel installed a temporary field modification to the 405 AM chip comparator to keep it in an operational status until such time as it can be sent to the factory for permanent modifications. On the 17th, 18th, and 23rd of August the [] technician returned to make minor adjustments to the machine. Since the 23rd of August, no maintenance adjustments of any sort have been made to the instrument except for water replenishment.

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2. TDS will observe the operational reliability of the comparators without making any maintenance adjustments until the reliability drops below acceptable limits in order to determine mean time between failure. As soon as sufficient data is available, a routine preventative maintenance schedule will be set up to keep all of the comparators in peak operating condition.

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3. At present, two [] comparators are [] for the modifications considered necessary to make them operationally reliable. It was originally estimated that these two machines would be returned to the NPIC by 1 October, but [] requested additional time because of difficulties with delivery of required optical subcomponents and additional design analysis work they were performing on the Interforemeters. It is now anticipated that the two [] will be returned by the middle of November, at which time two more machines will be returned for modifications, if the funding is available.

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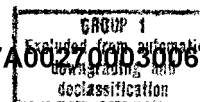
4. Based on the performance record [] for the past two months, indications are that the fundamental operational reliability problems with the chip comparators have been resolved, but that additional funding will be required. A cost analysis and work statement has been received [] and is being forwarded to the respective components under separate memo for action.

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Declass Review by NIMA / DoD

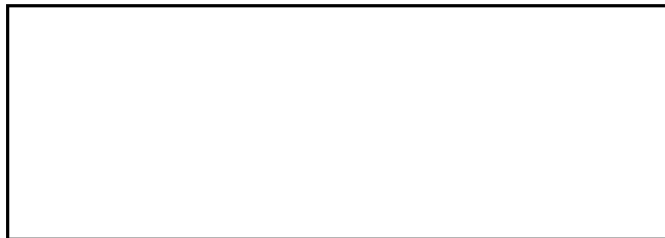
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5. An additional consideration concerning the operational use of the chip comparators, not previously covered, is the availability of computer time and software. Since four out of the six chip comparators are exclusively on-line, the operational availability of the chip comparators must therefore take into consideration the data processing as an essential element of consideration. It is my understanding from talking to IAS, TDS, and DIA personnel that that the data processing problem is of extreme concern to them. Also affecting the operational acceptability of the chip comparators are the building facility requirements and training programs. Since the chip comparators are highly sophisticated pieces of equipment, they impose special building facility and training requirements on the Center.

6. In summary, TDS feels that the solution for the basic reliability and maintainability of the chip comparators has been resolved from the technical viewpoint, but that the operational acceptability is dependent upon additional considerations beyond its control; namely, the required additional funding, the data processing capability, training, and building facility maintenance.



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CENTER ROUTING SLIP

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TO	INITIALS	DATE	REMARKS	
DIR			In view of the interest concerning the operational status of the chip comparators, the attached memo for the record is being forwarded to keep you up to date on TDS progress in th <div style="border: 1px solid black; width: 200px; height: 100px; margin: 10px auto;"></div>	
DEP/DIR				
EXEC/DIR				
TECH ADV	<i>X</i>	<i>JNC 10/31</i>		
SPECIAL ASST				
CH/SS				
ASST FOR P&M				
ASST FOR OPS				
ASST FOR PA				
ASST FOR TD	<i>✓</i>			
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